

FIG. 1A

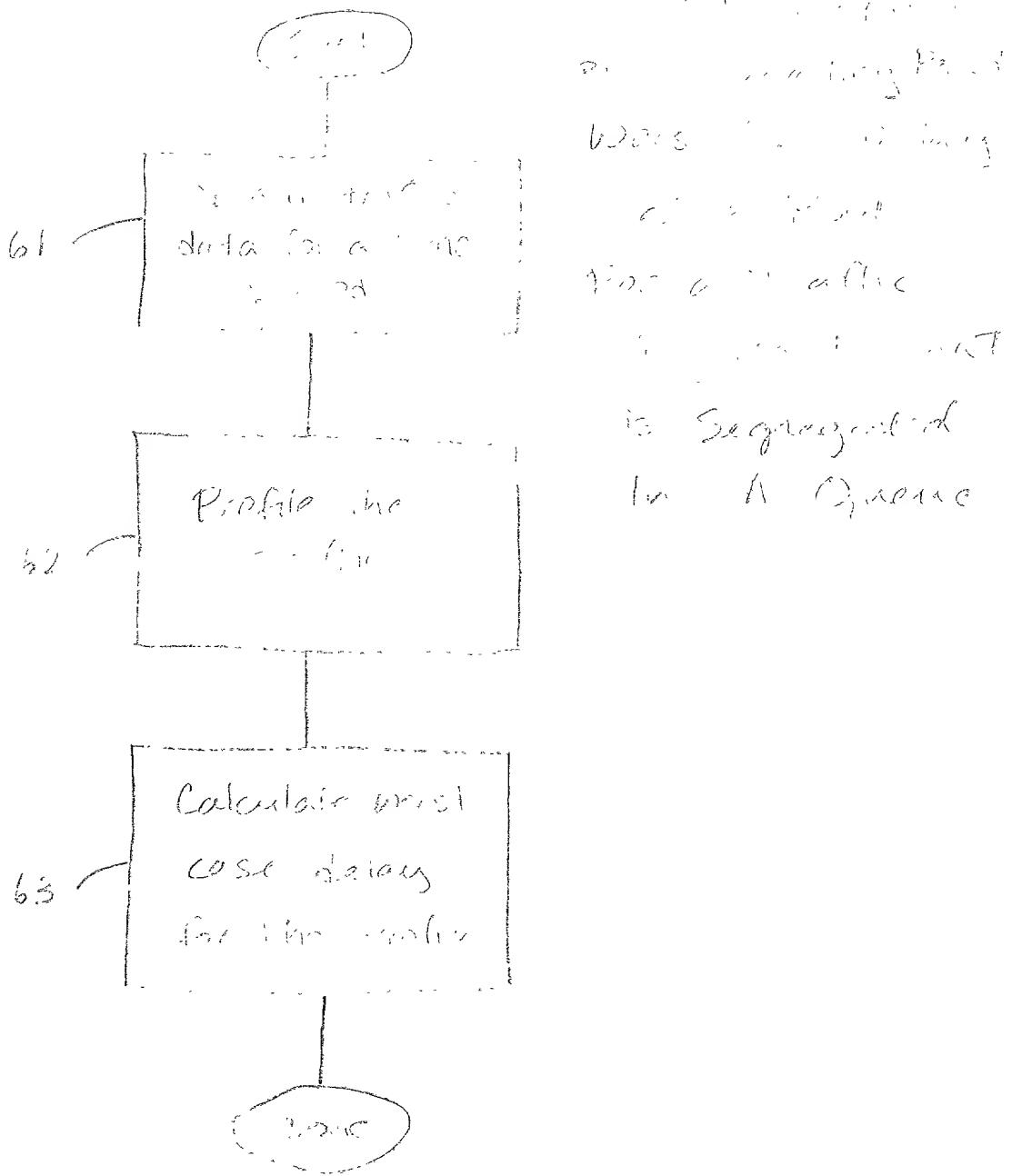


FIG 1B

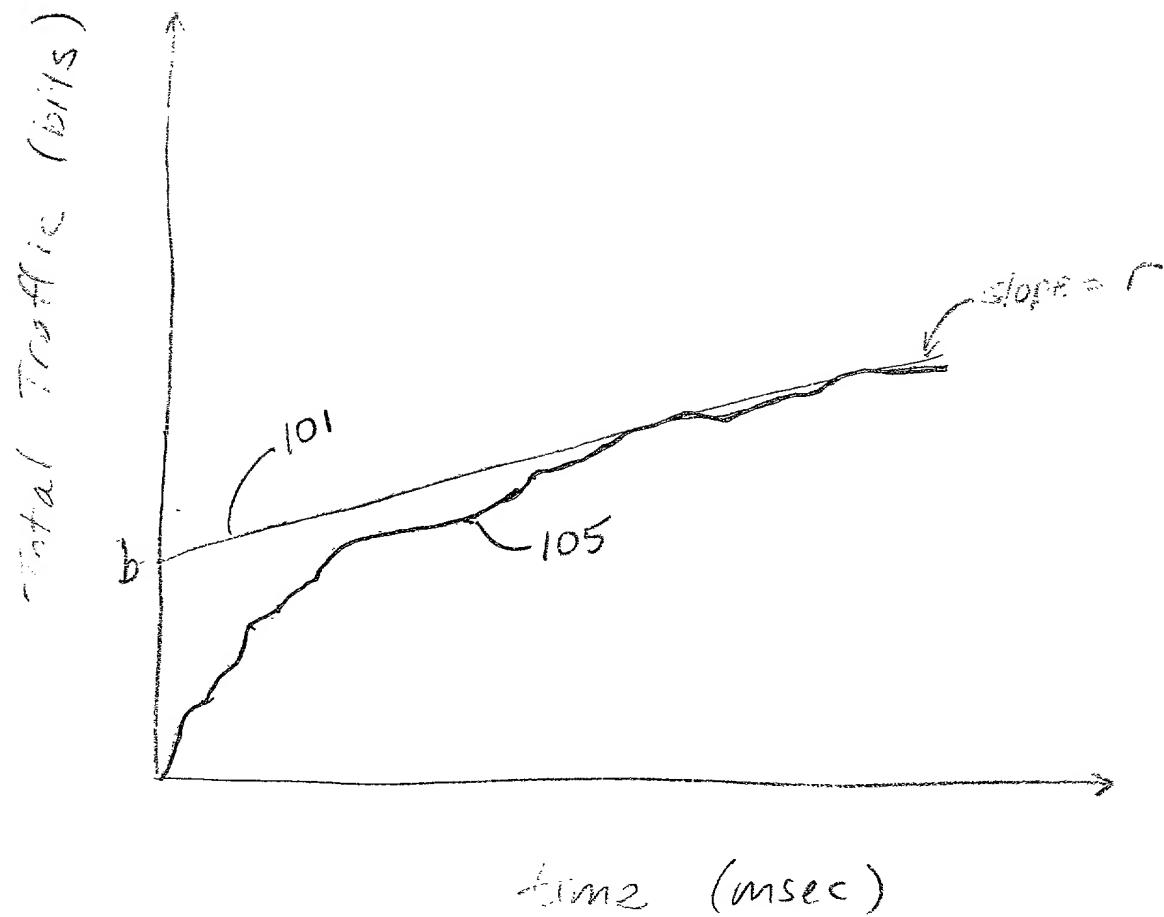


FIG. 2

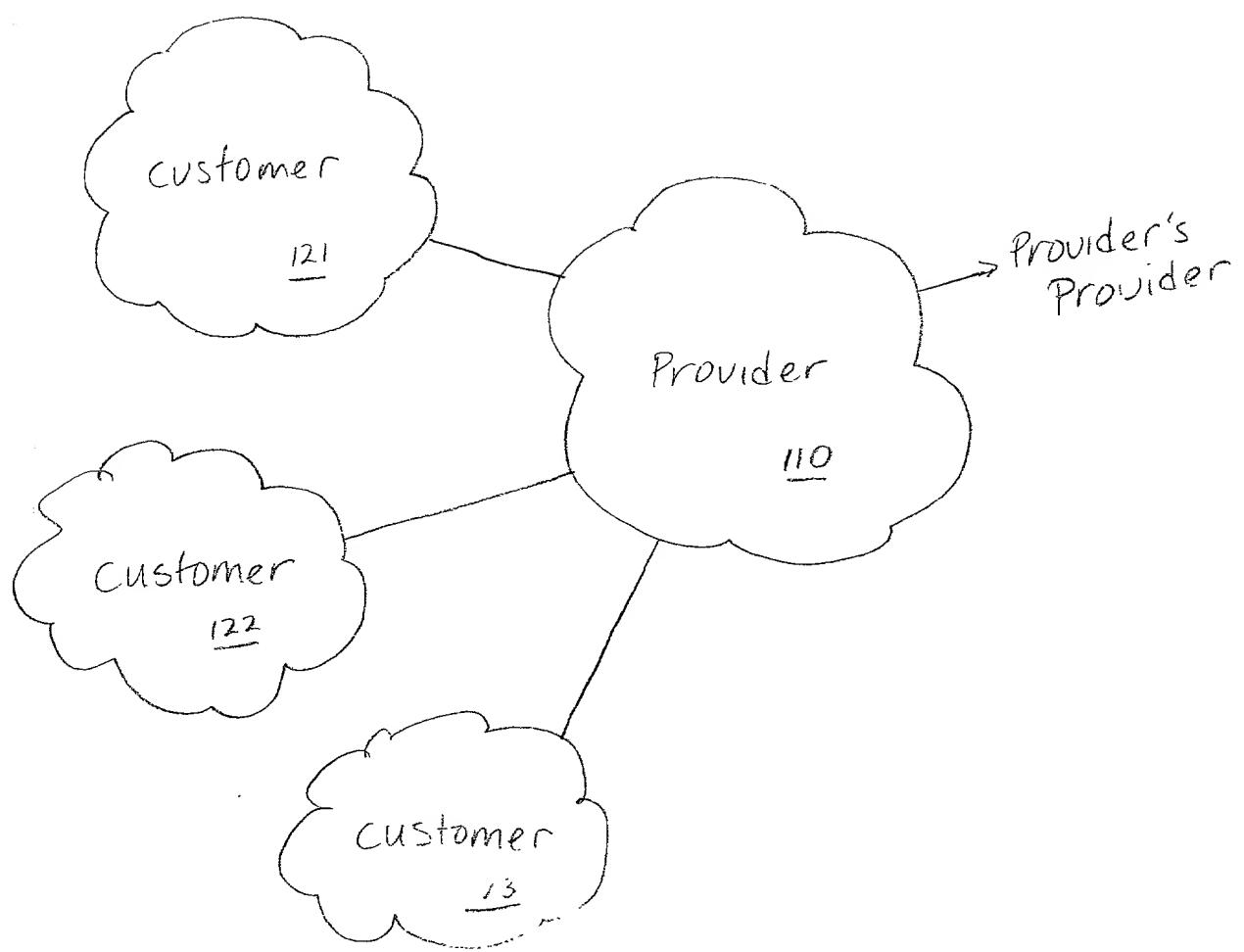
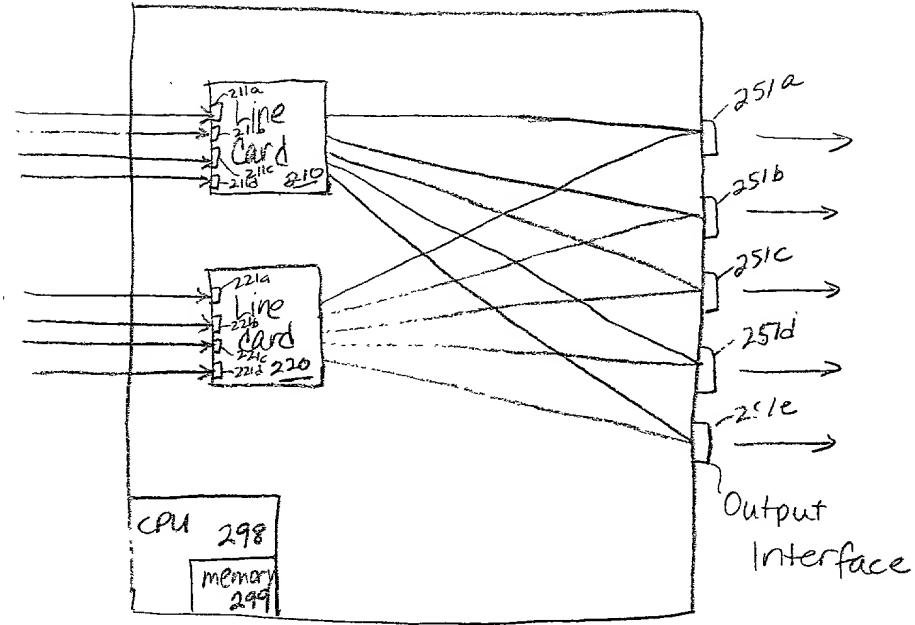


Fig 3

Simplified Sige Tower



↗
20D

FIG. 4

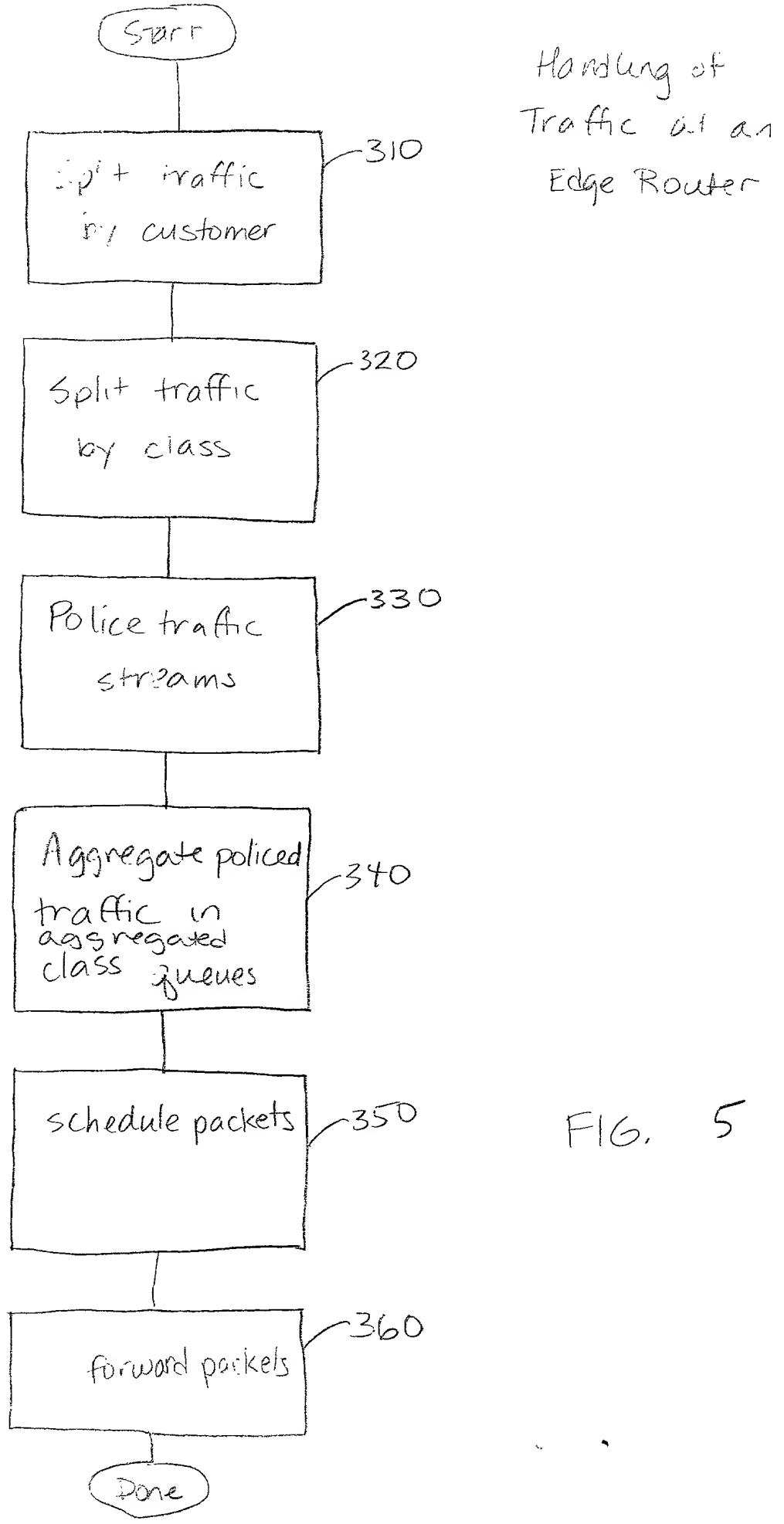


FIG. 5

Conquer P. m. *lutea* C. L. Smith

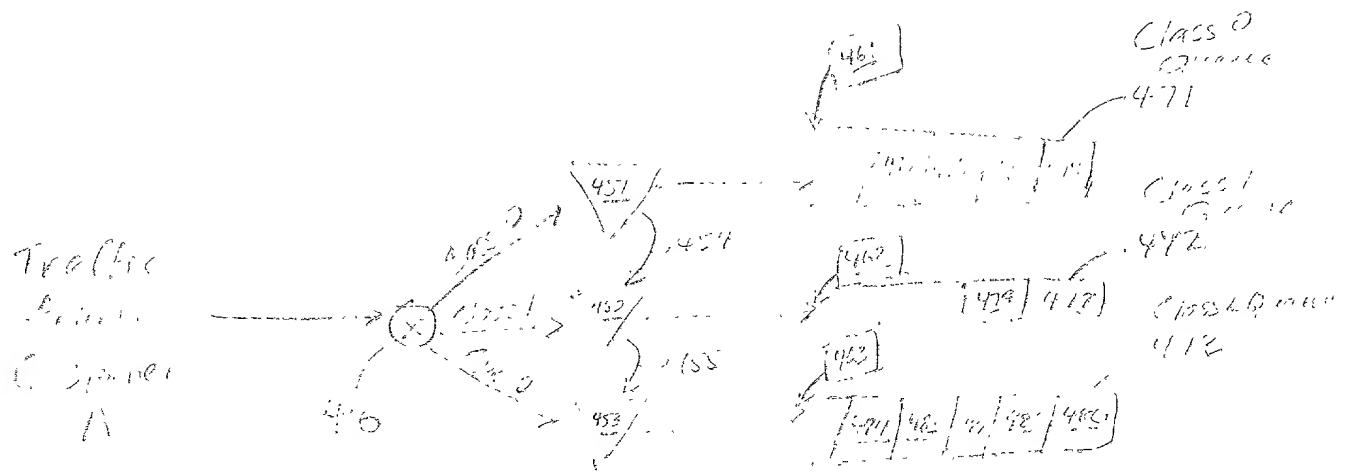
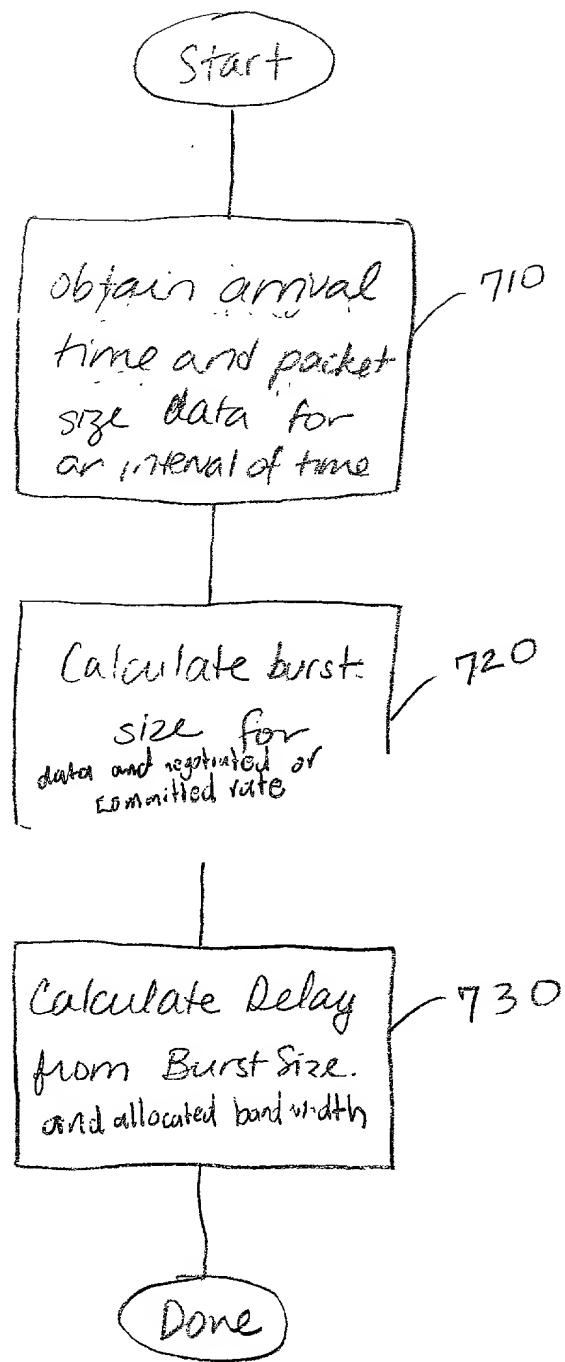


FIG 6.



Estimating
Worst Case
Delay at a Node

FIG 7

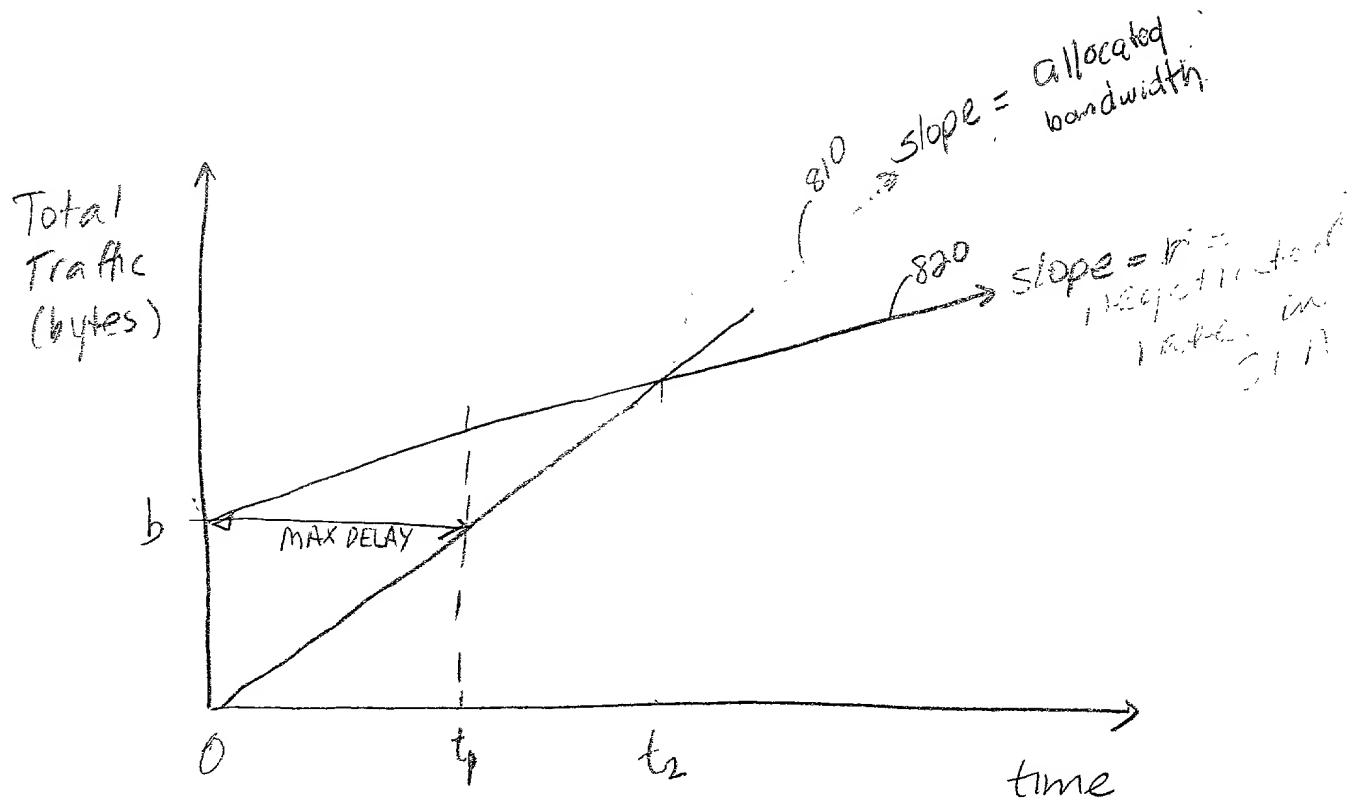


FIG. 8

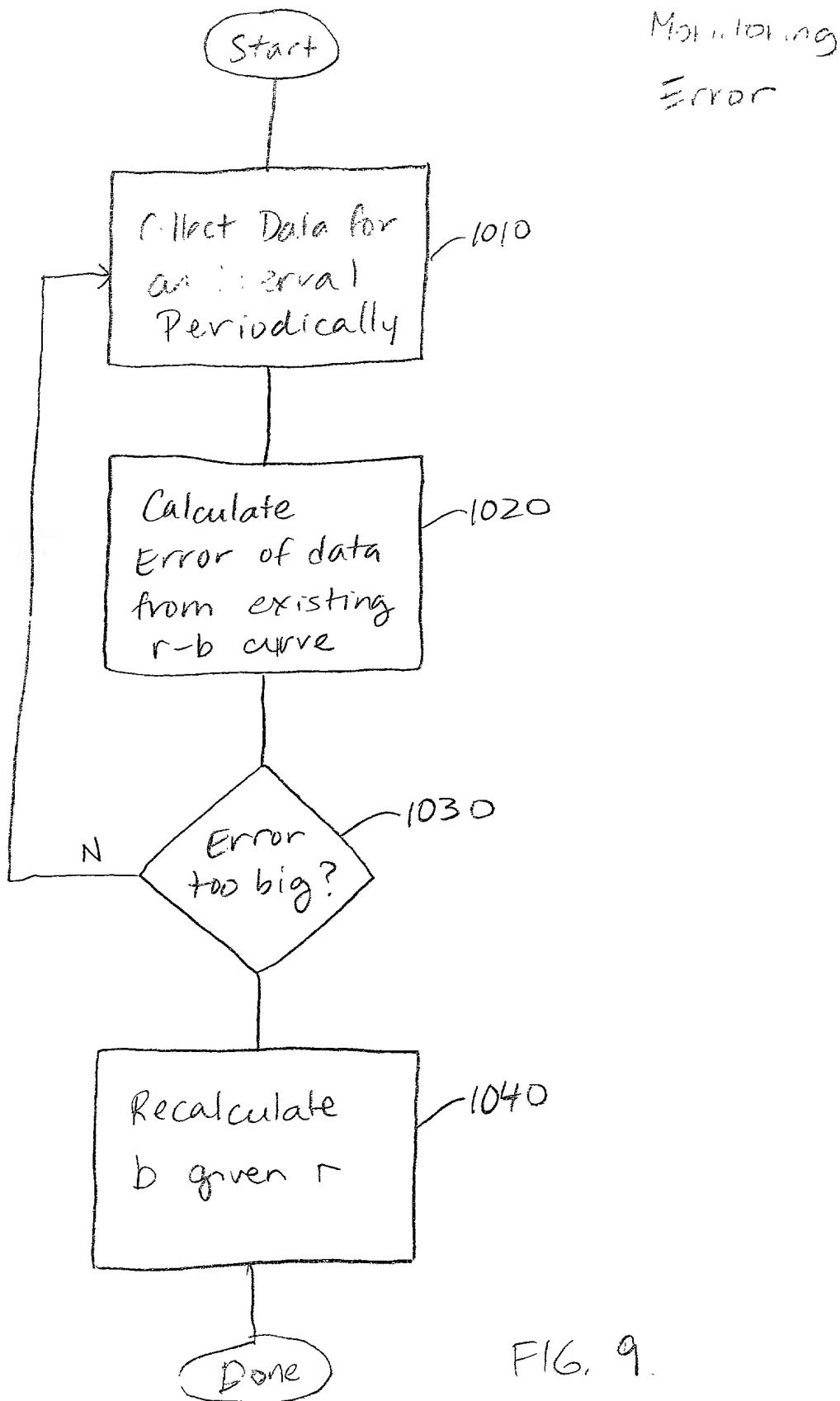


FIG. 9.

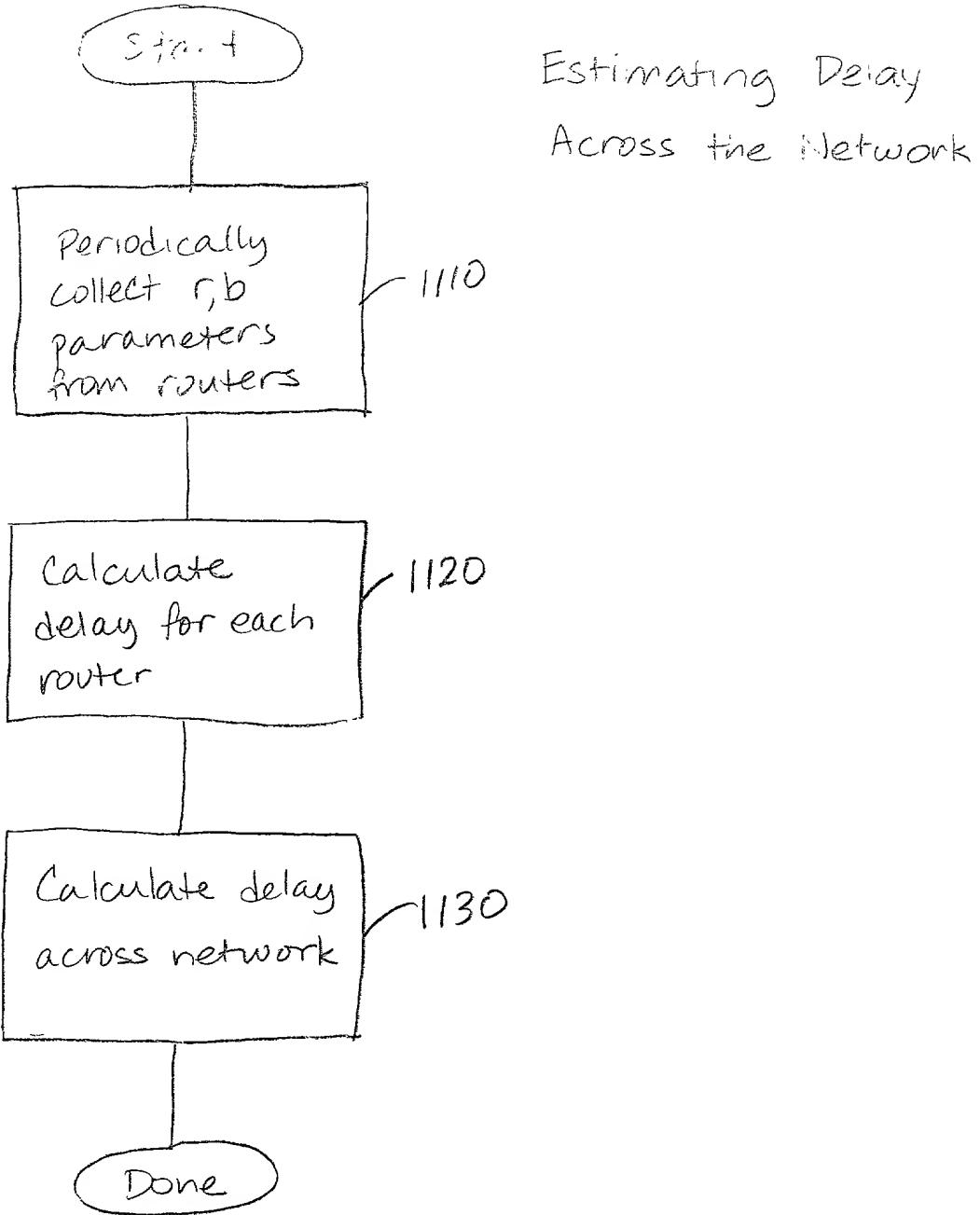


FIG. 1D

Allocable memory

Virtual memory

Process



Contiguous block

→ segmented

→ virtual and

→ allocated for
→ pages or segments

Contiguous block

containing different

allocation of link

or different size

or different allocated by



{ 16, 11 }